

FIG. 1

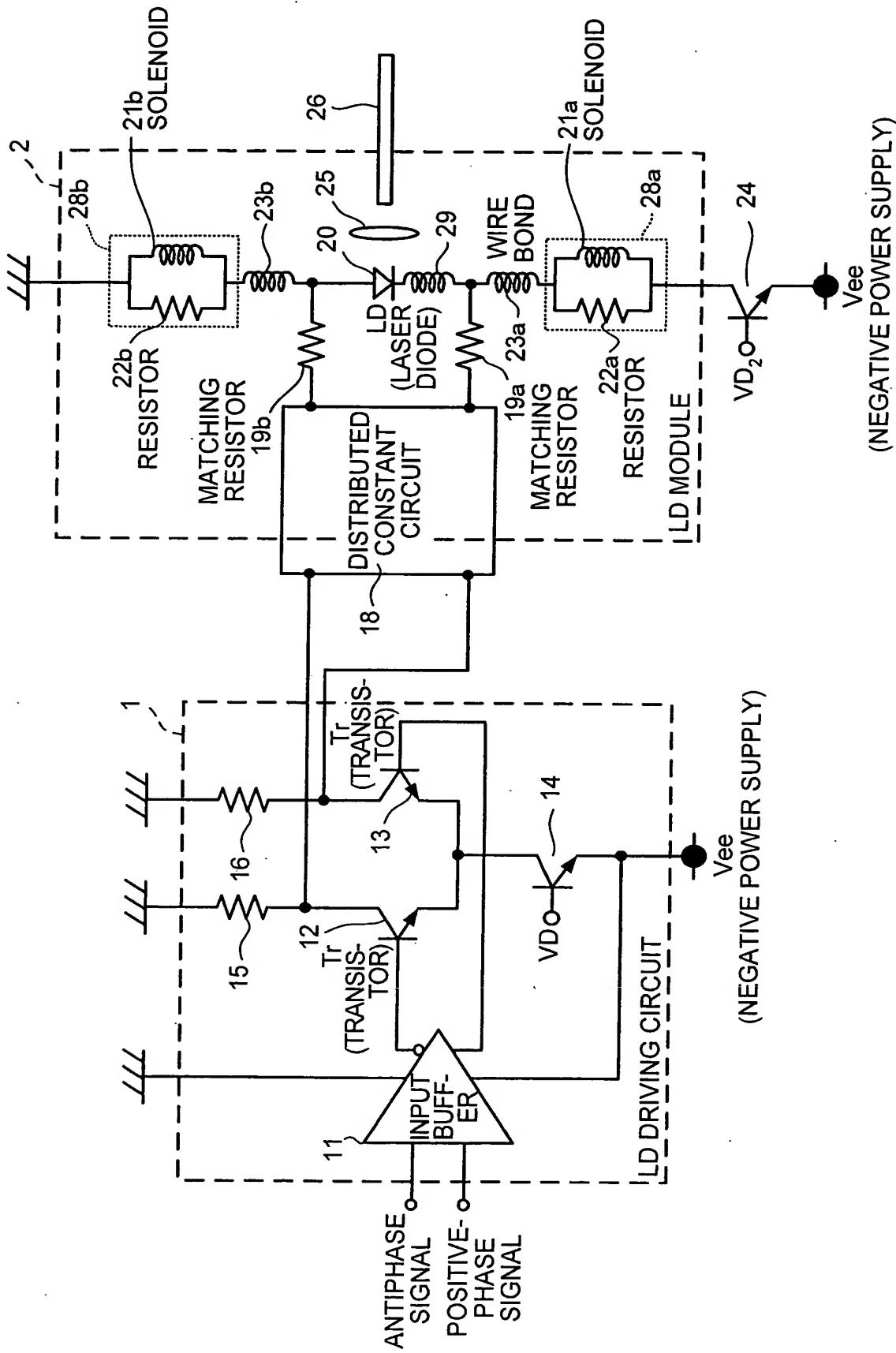


FIG.2

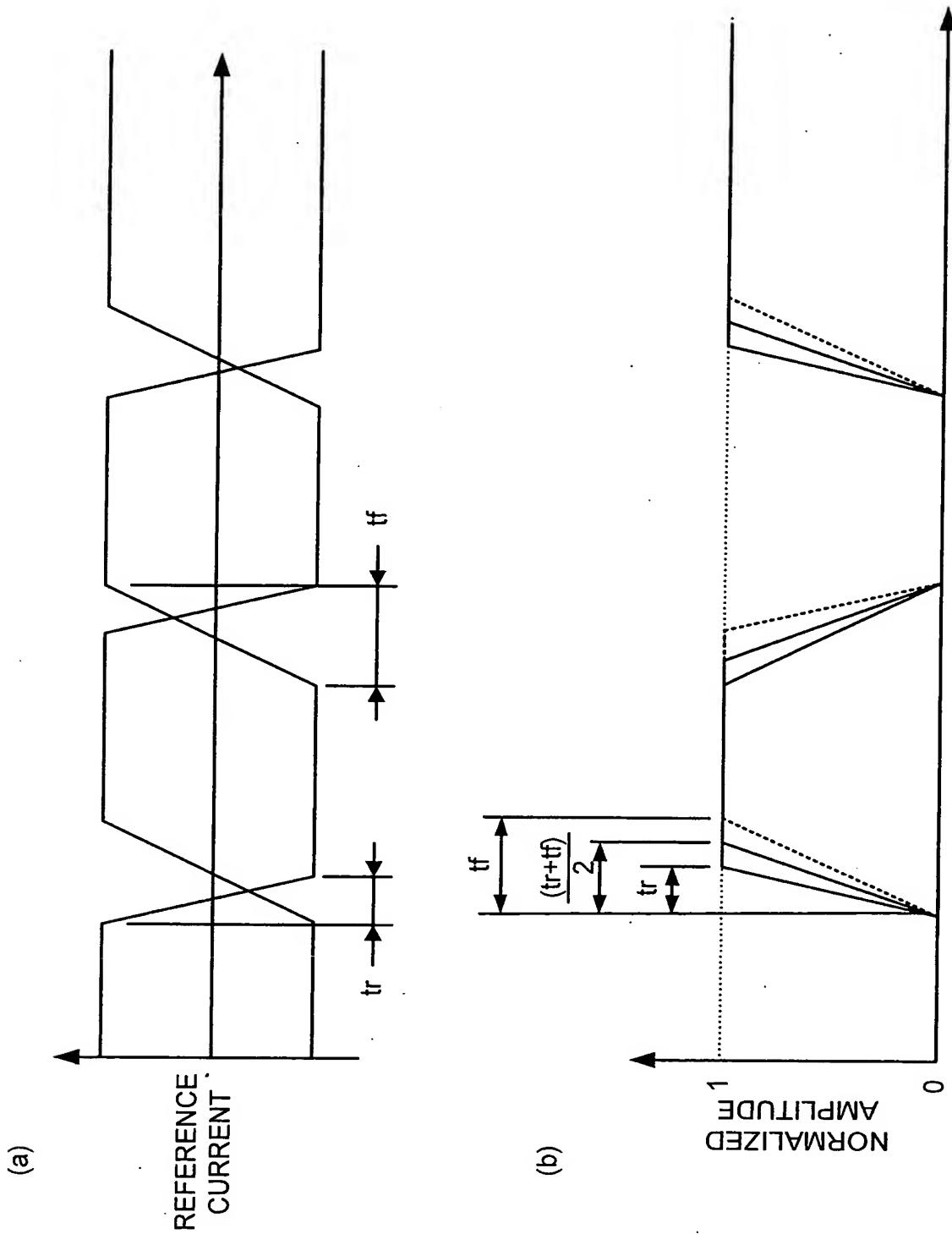
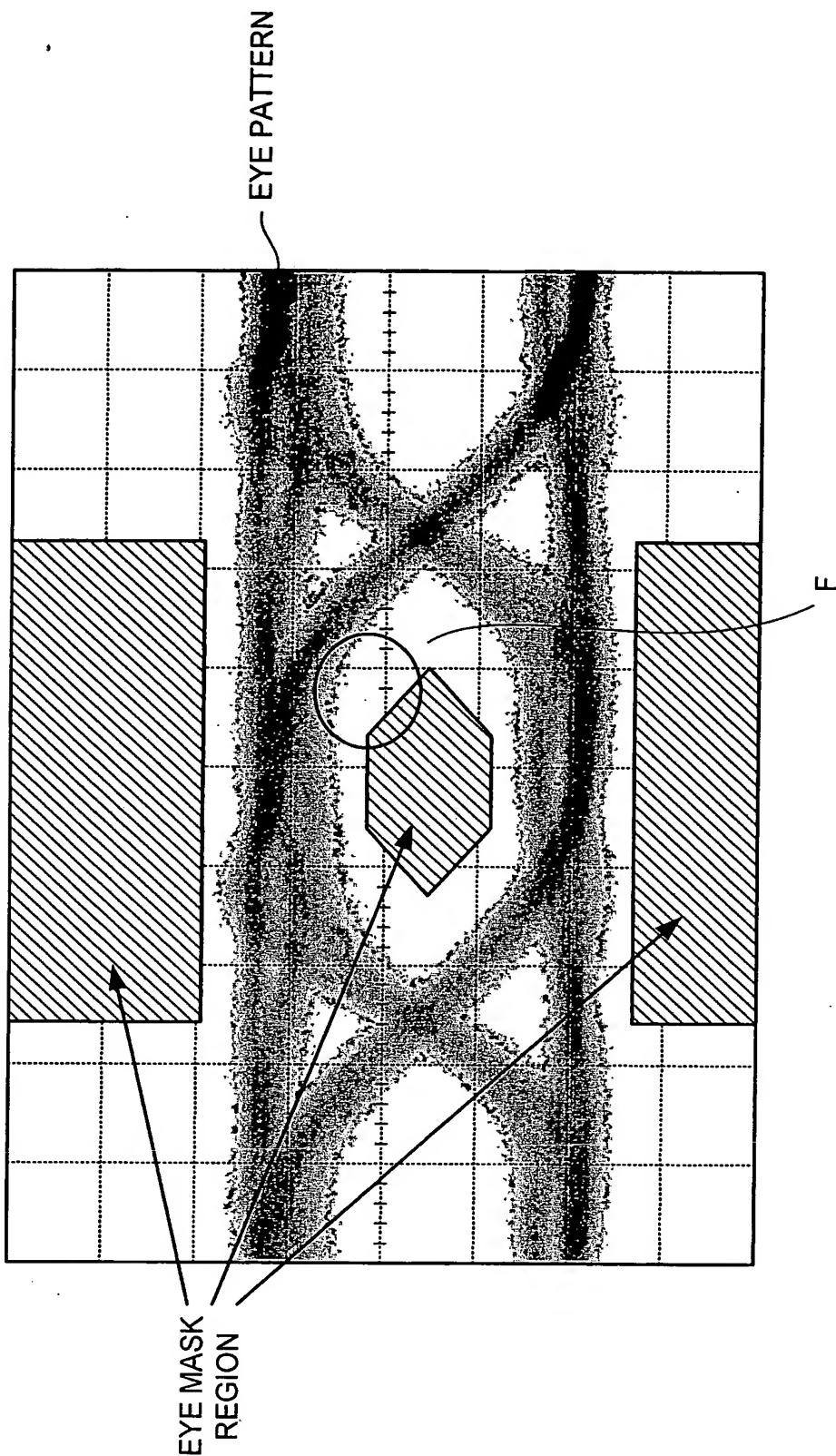


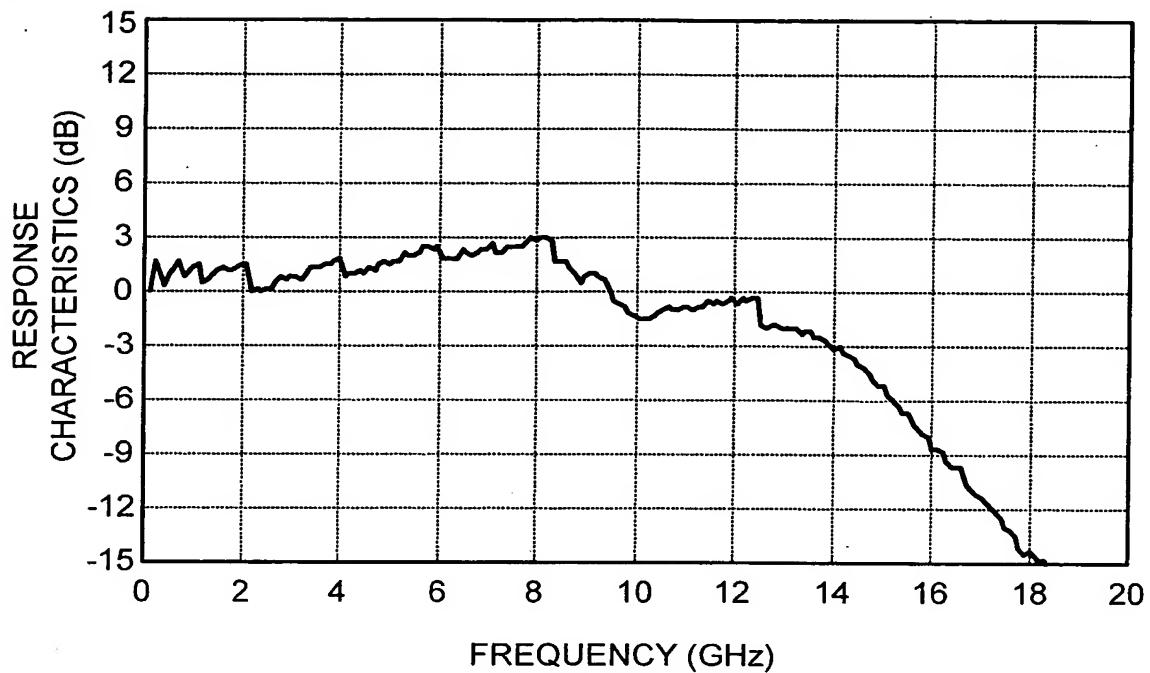
FIG.3



BEST AVAILABLE COPY

FIG.4

(a)



(b)

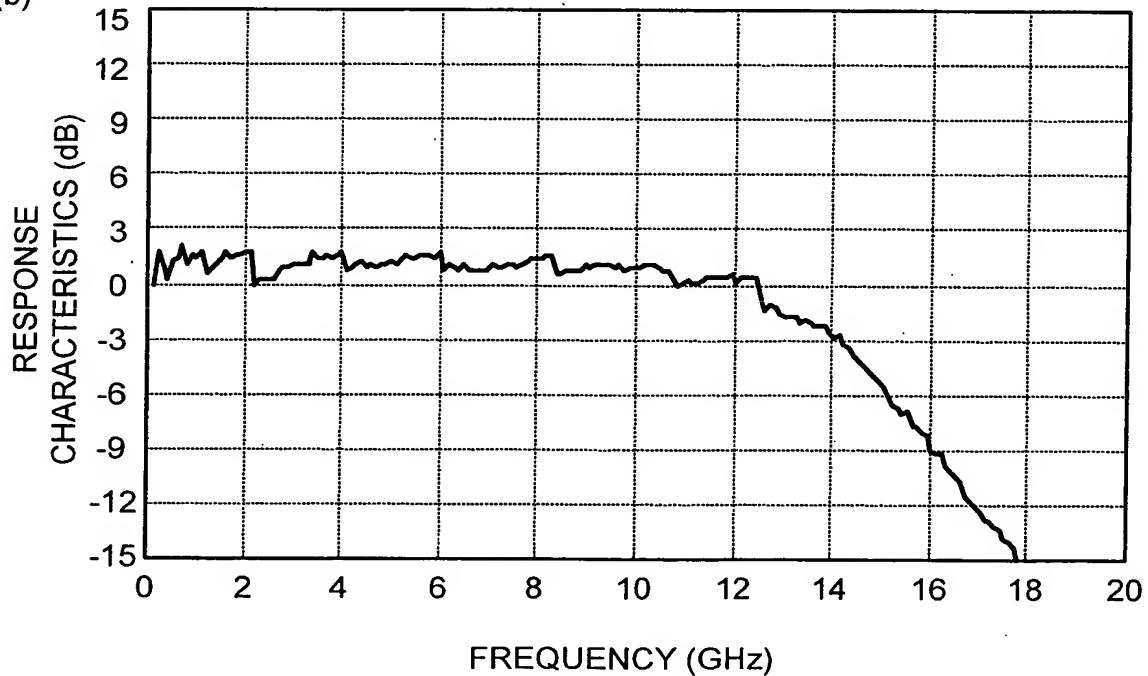


FIG.5

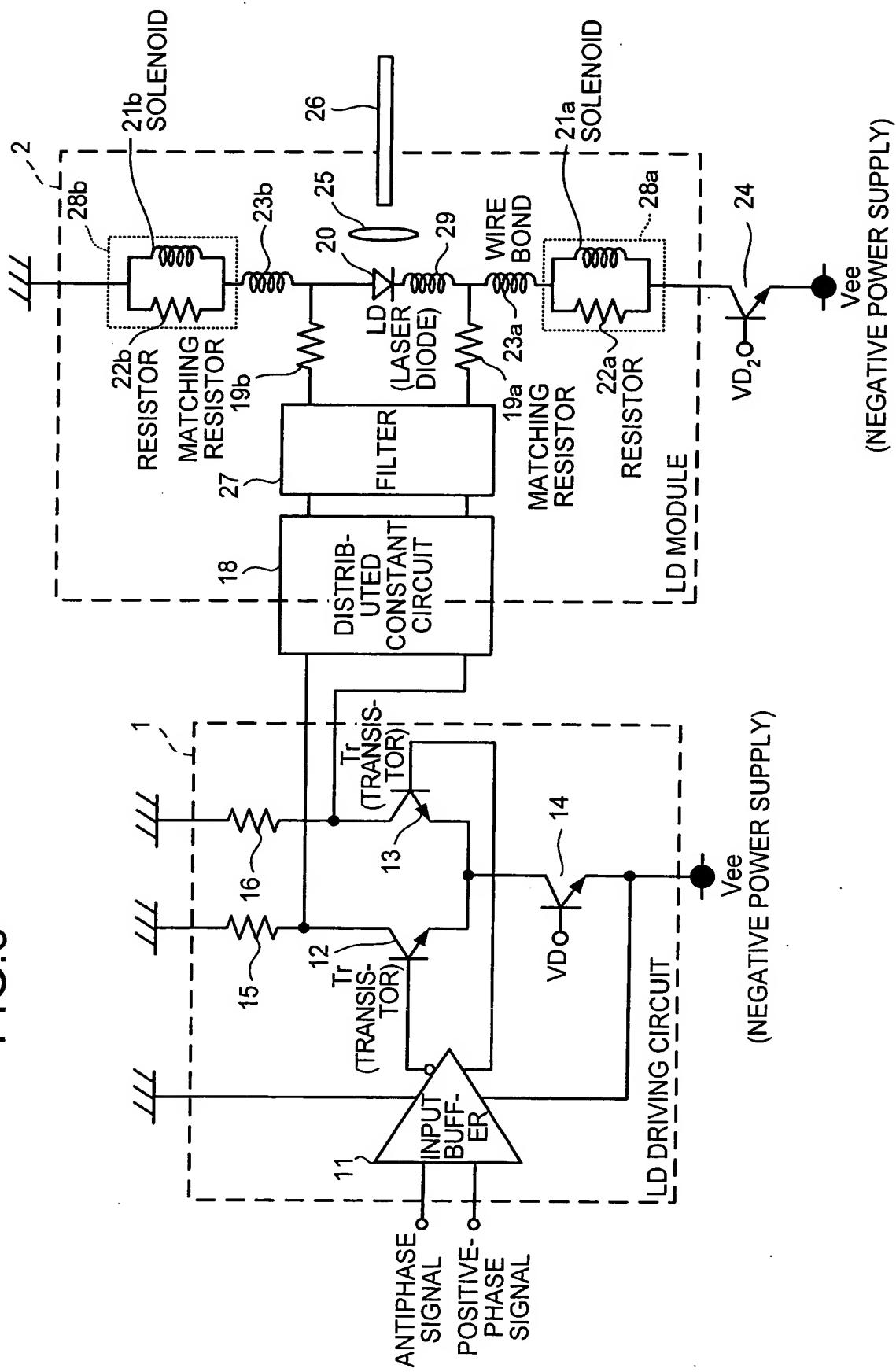


FIG.6

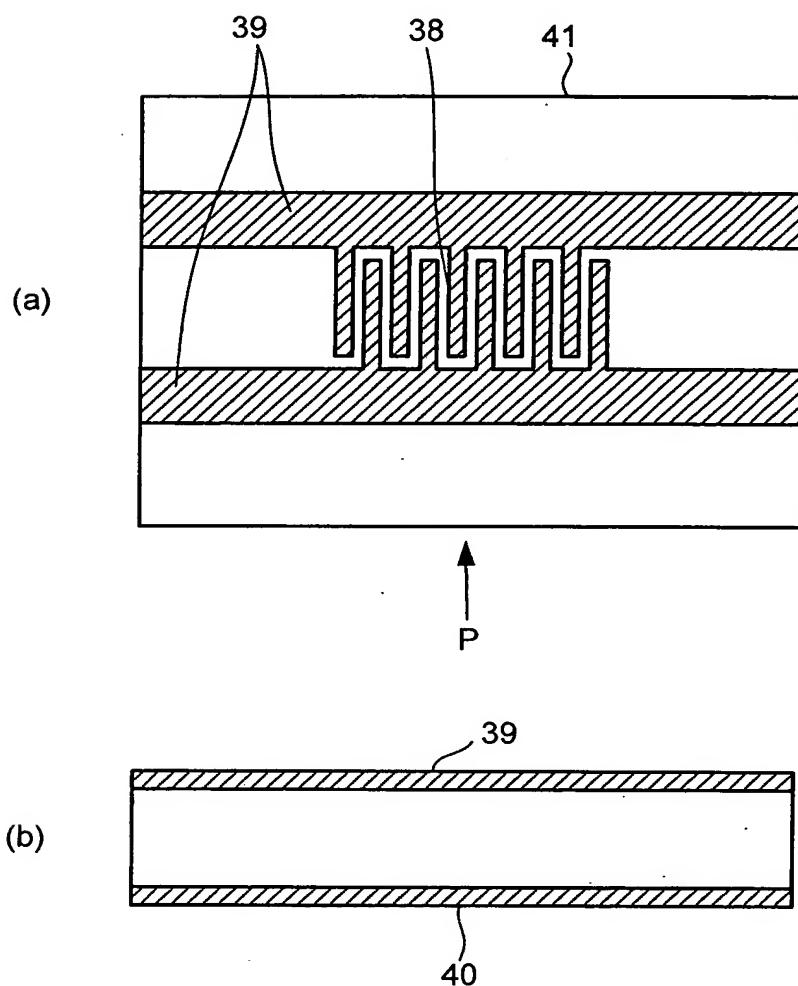


FIG. 7

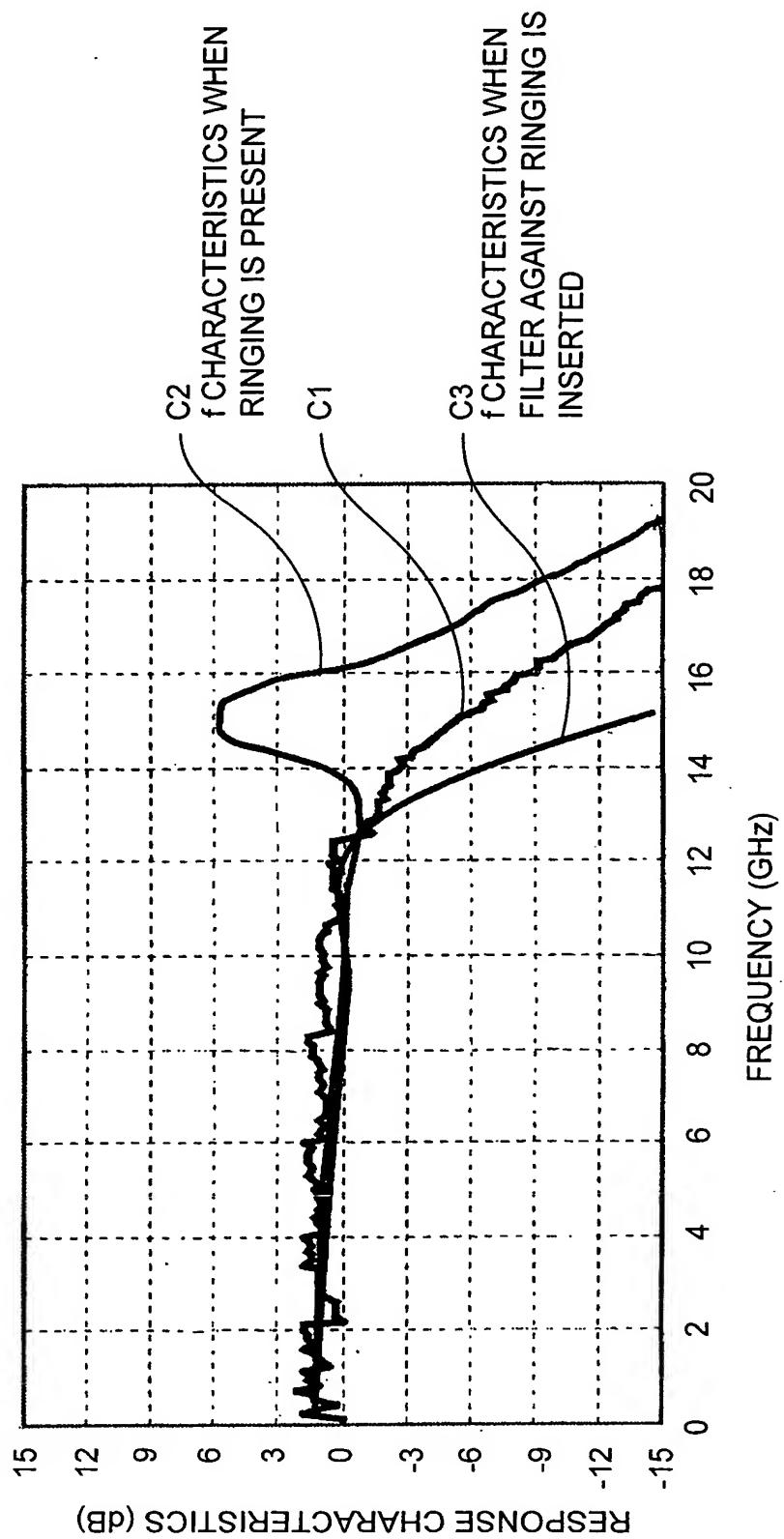


FIG.8

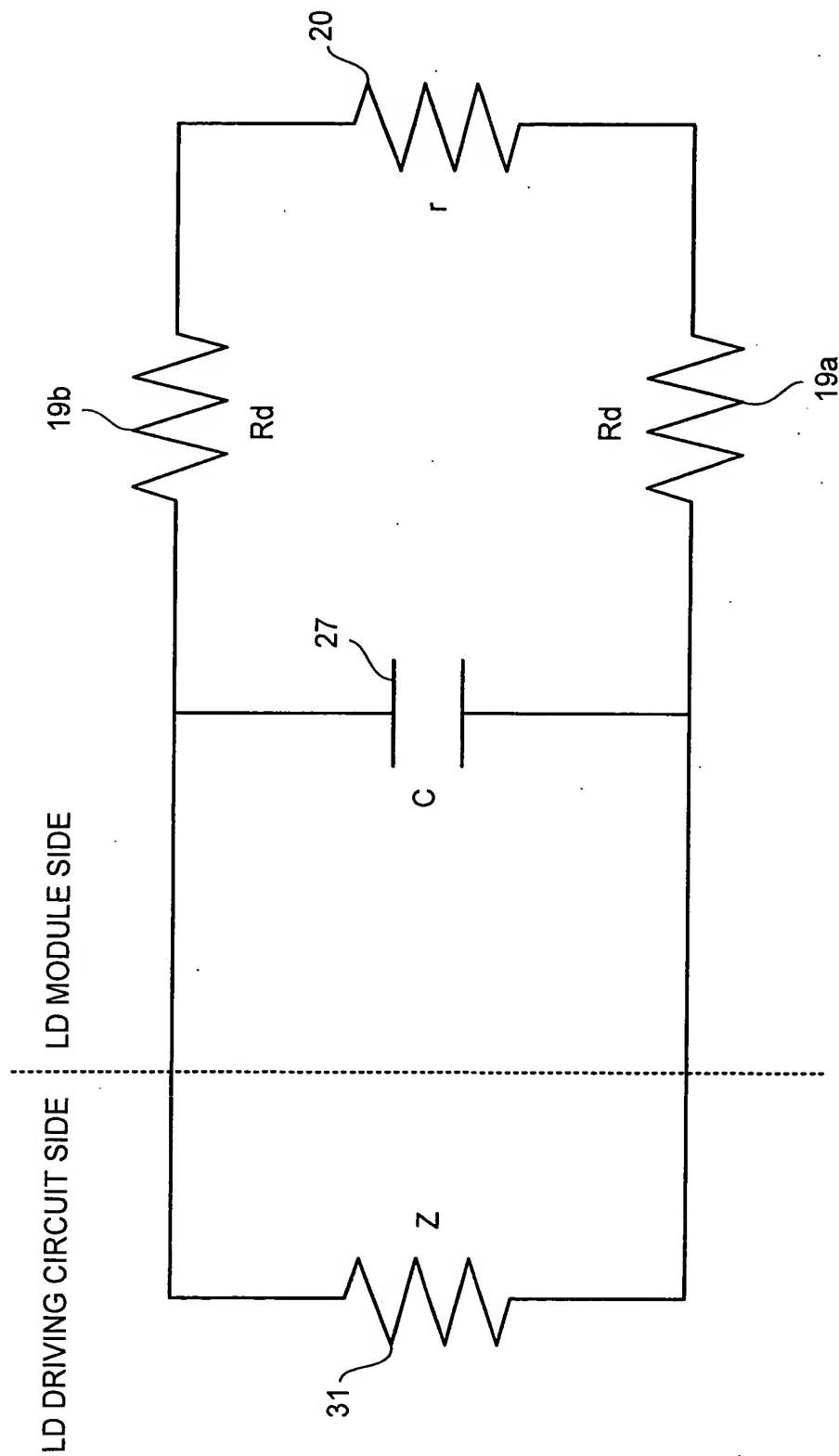
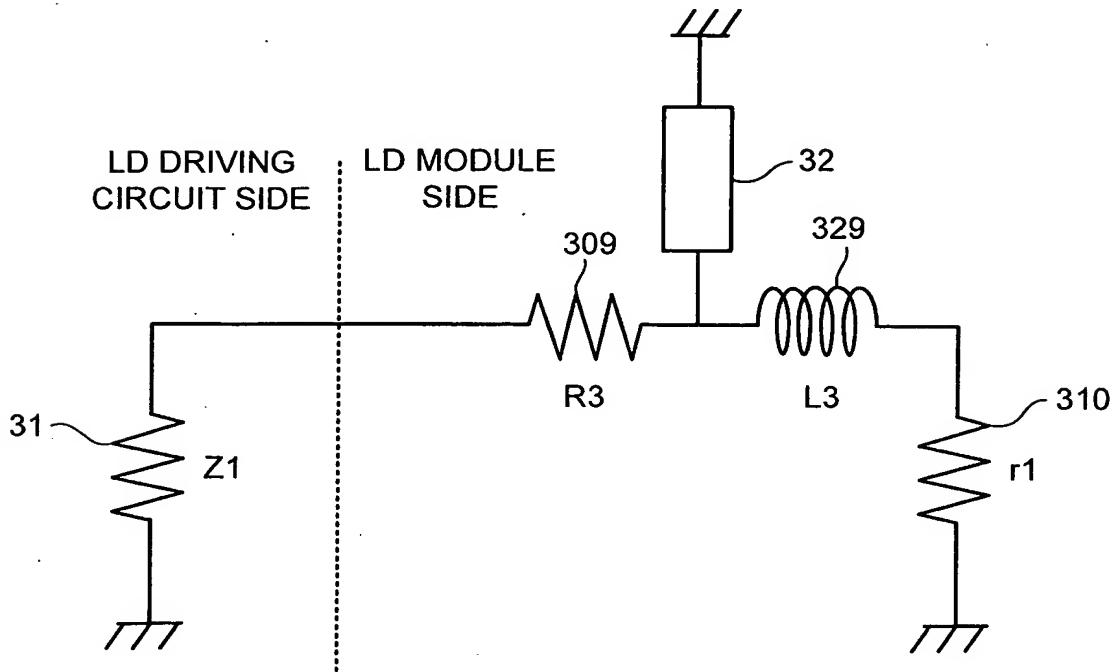


FIG.9

(a)

LD DRIVING
CIRCUIT SIDE

LD MODULE
SIDE



(b)

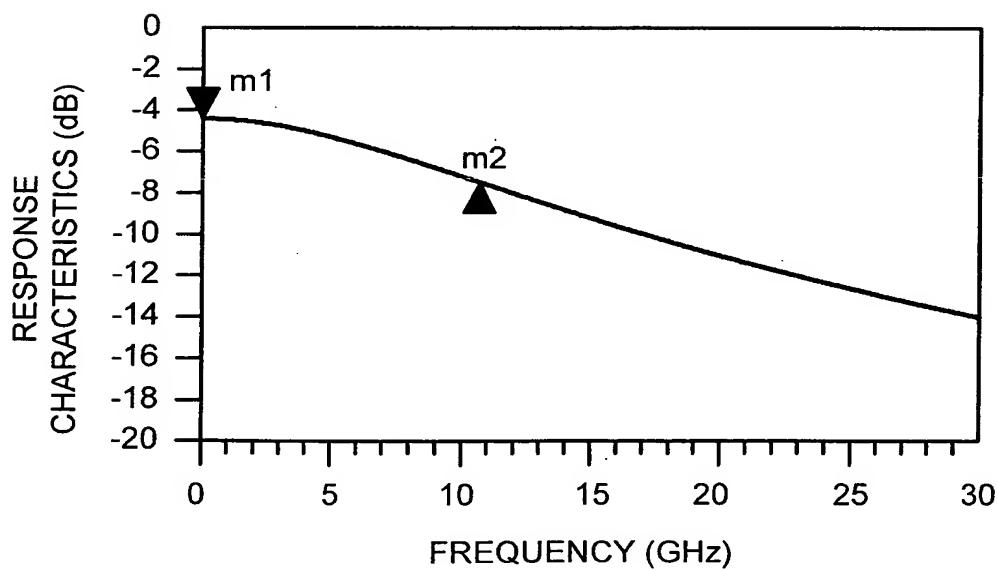


FIG.10

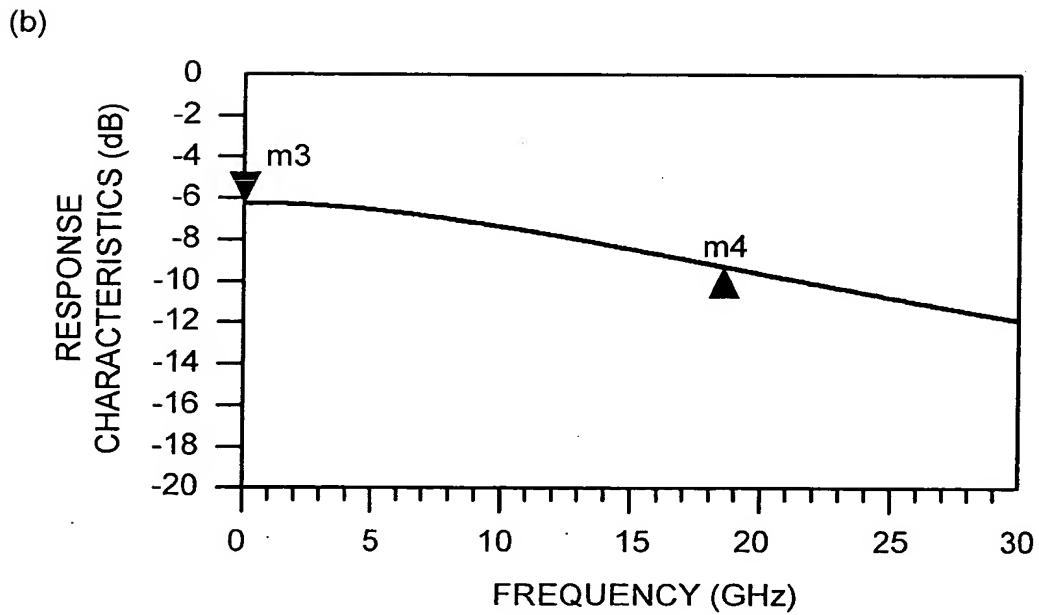
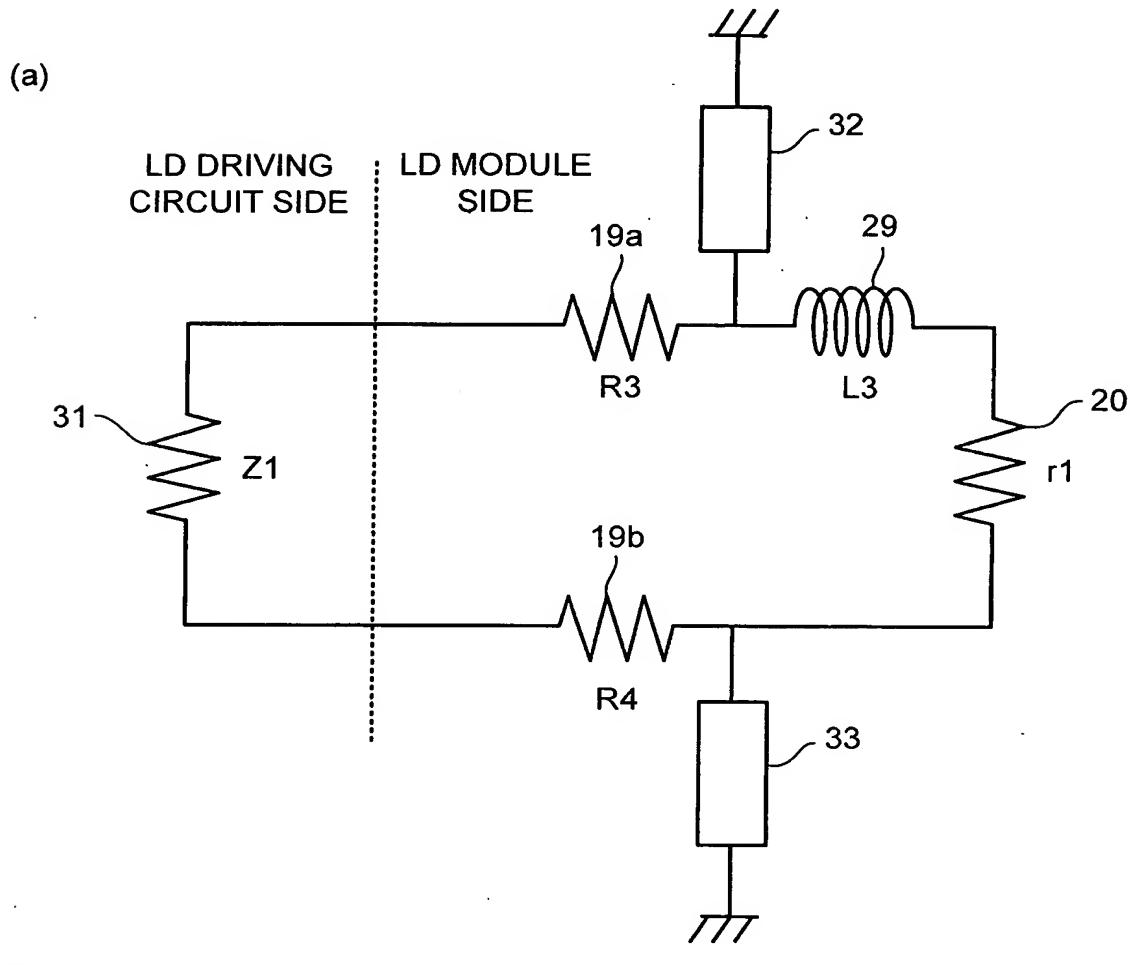
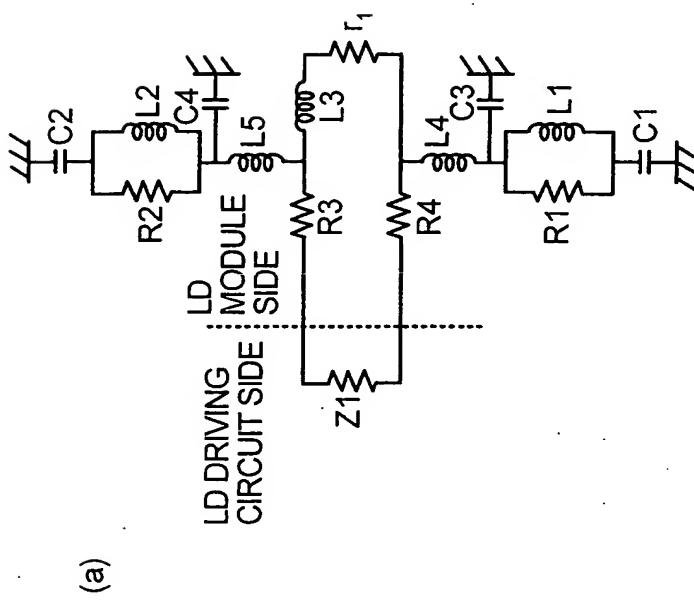
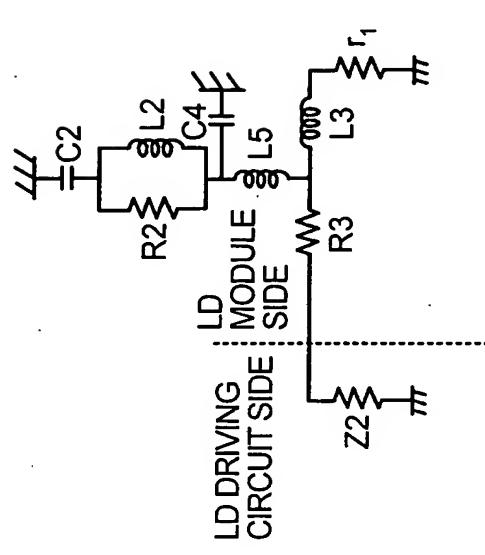


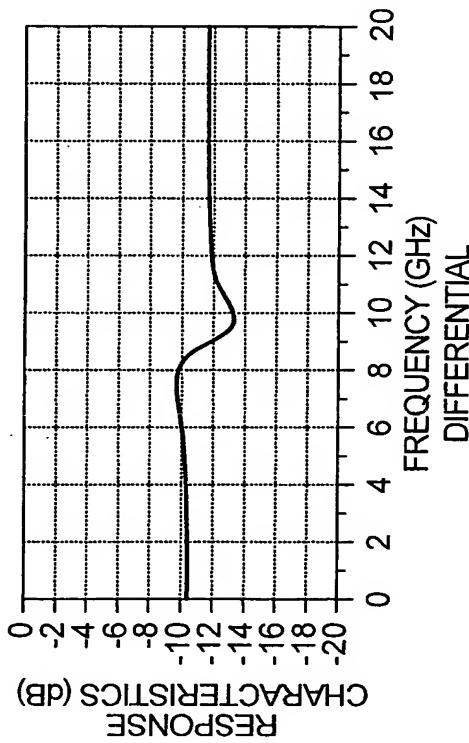
FIG. 11



(c)



(b)



(d)

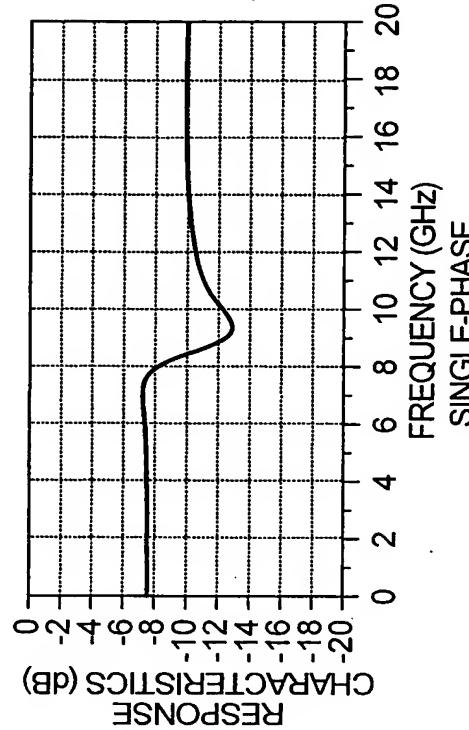
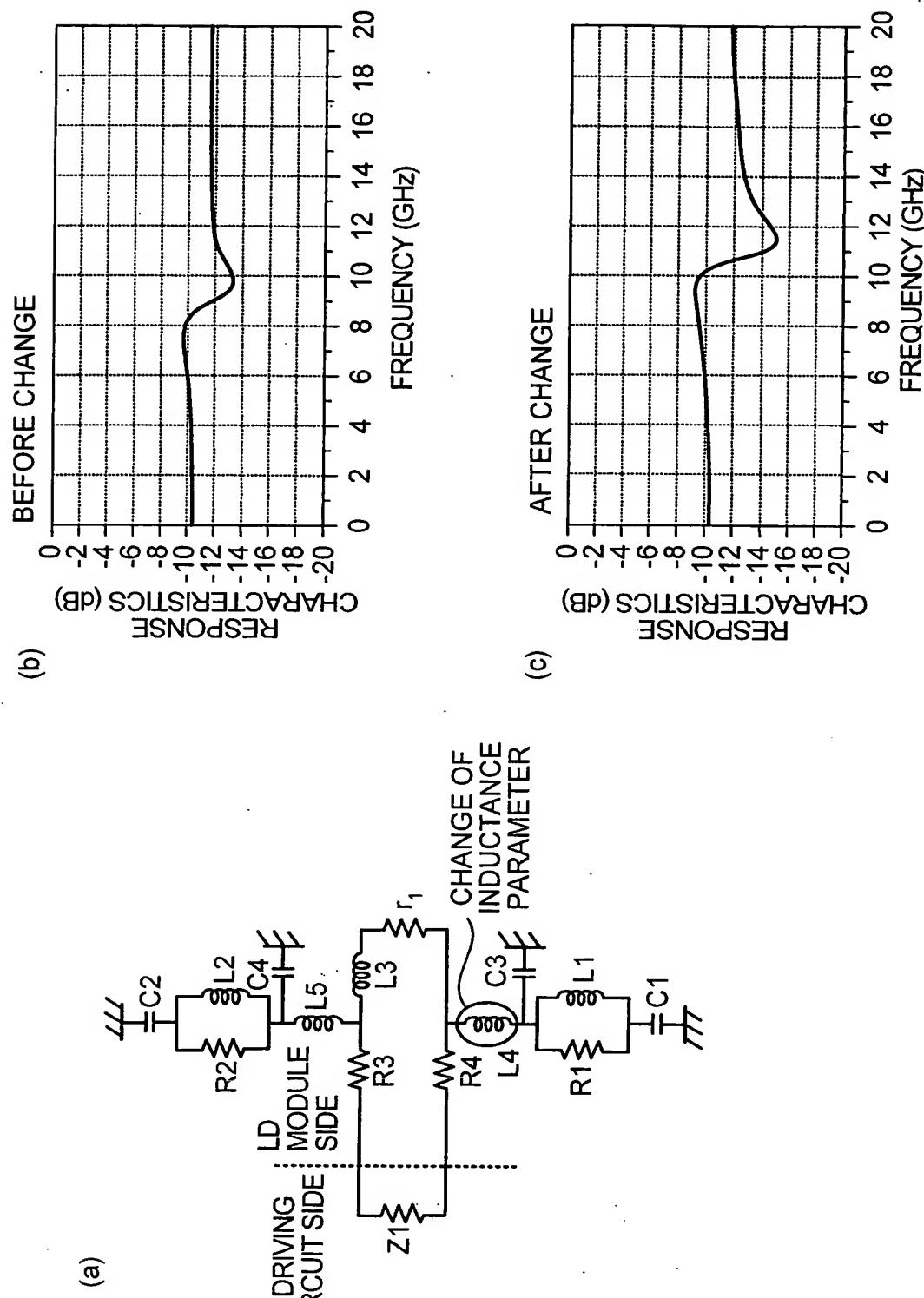
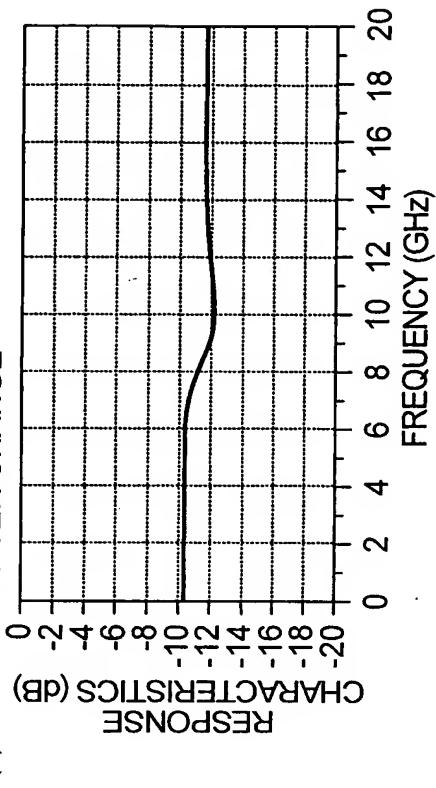


FIG. 12





(b) BEFORE CHANGE

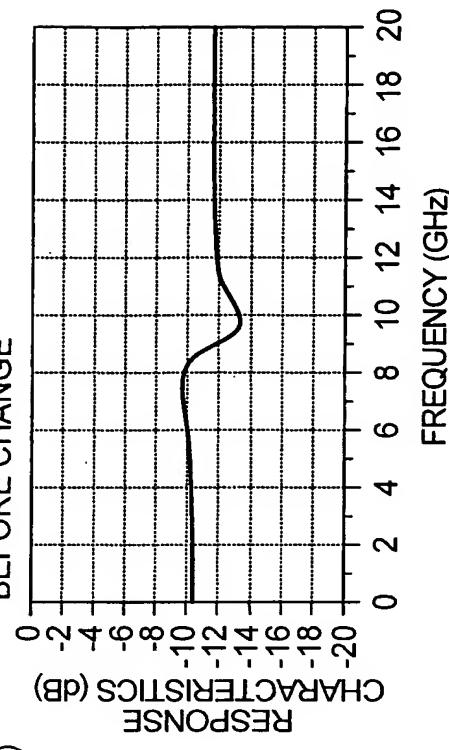


FIG. 13

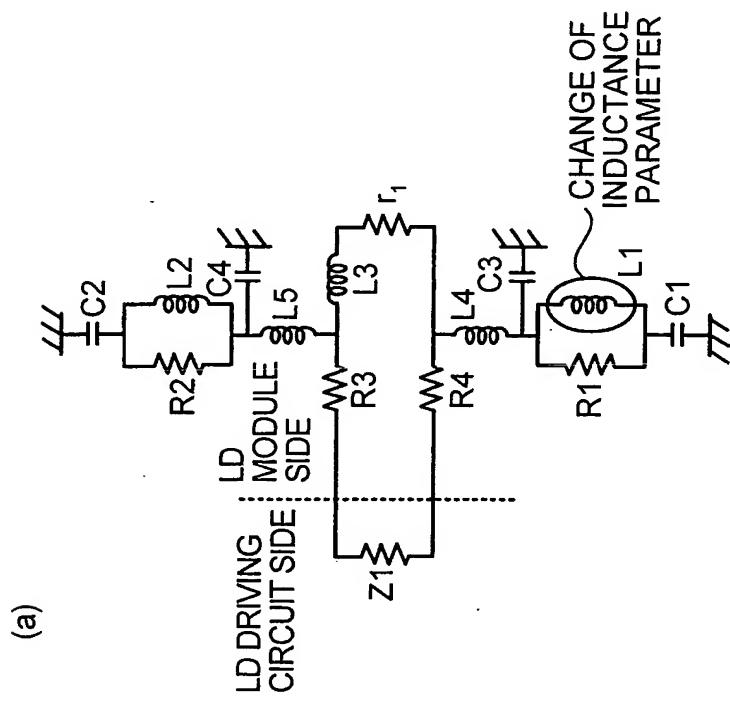
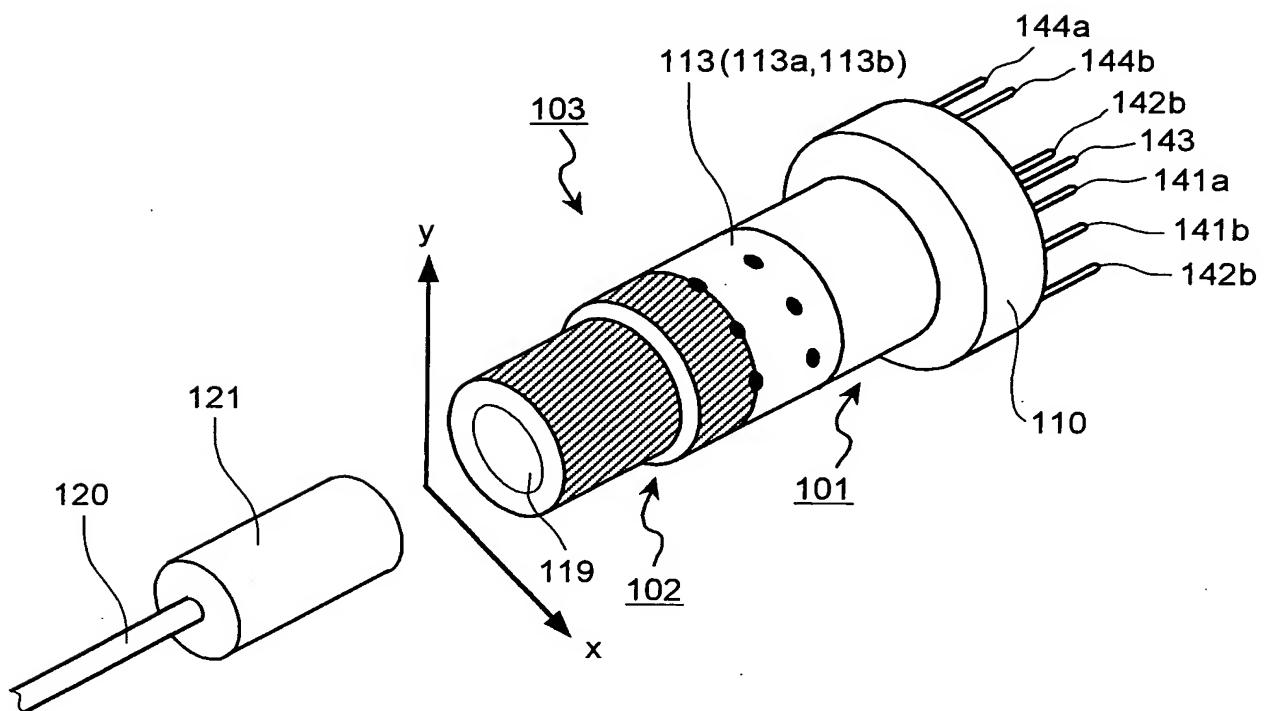


FIG. 14



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FIG. 15

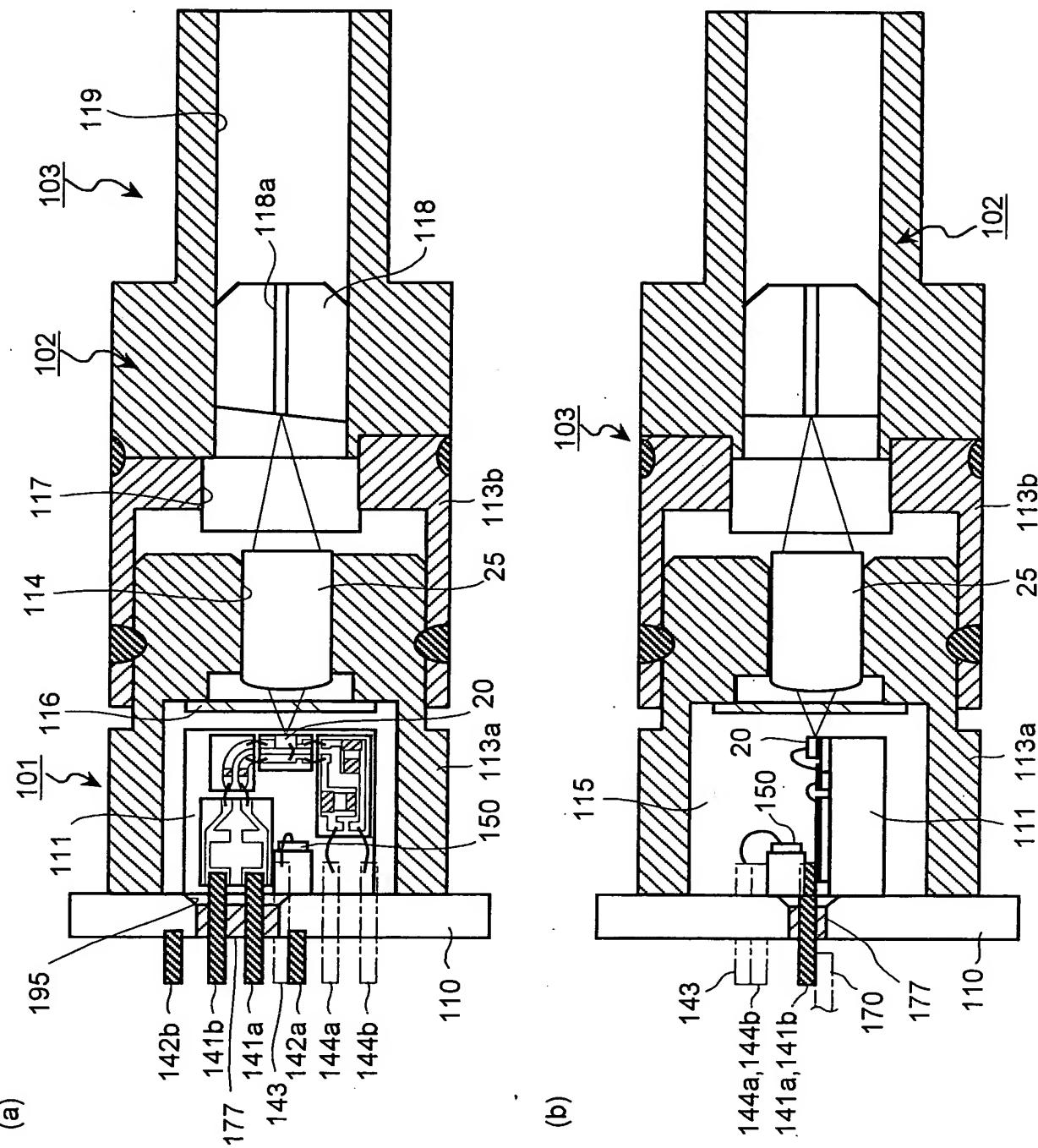


FIG. 16

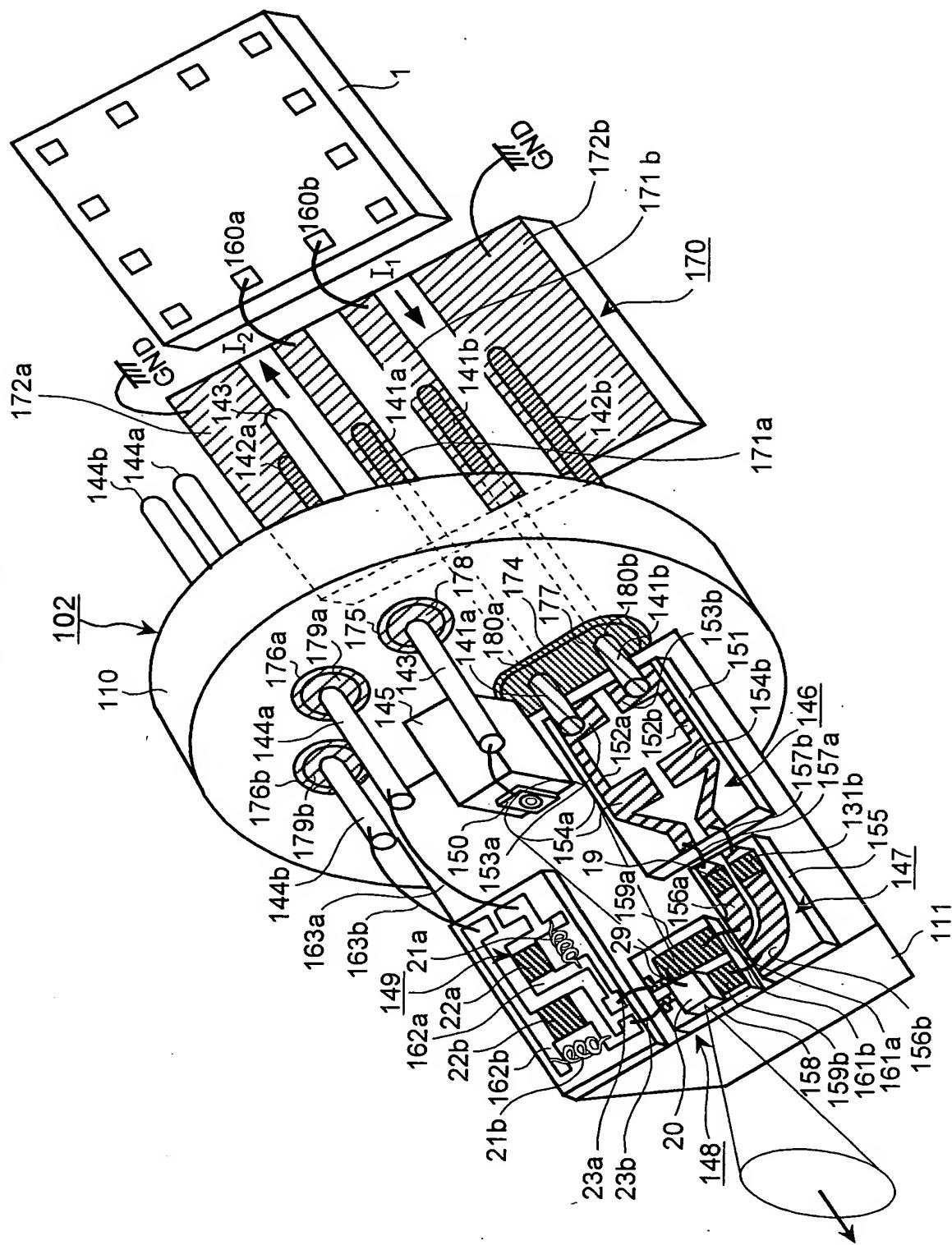
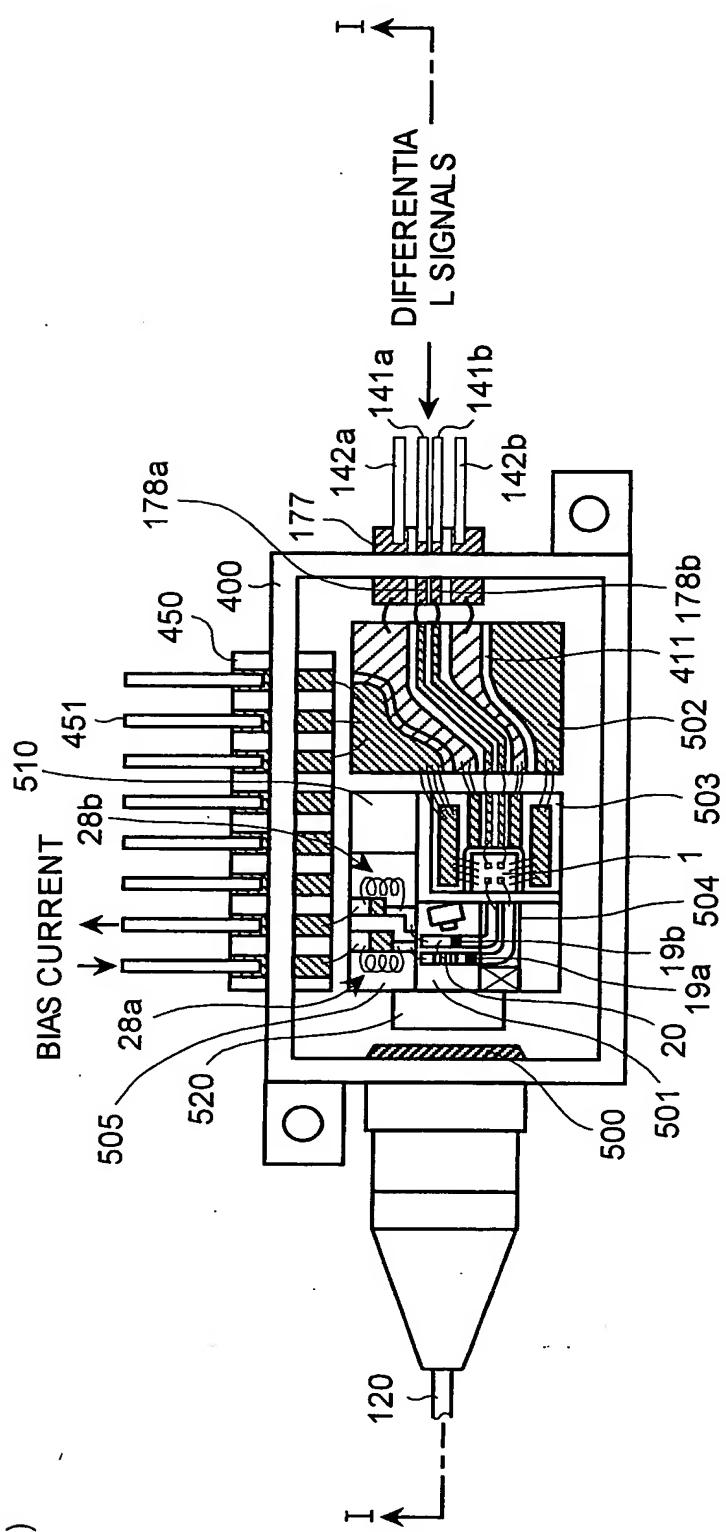


FIG. 17 (a)



(b)

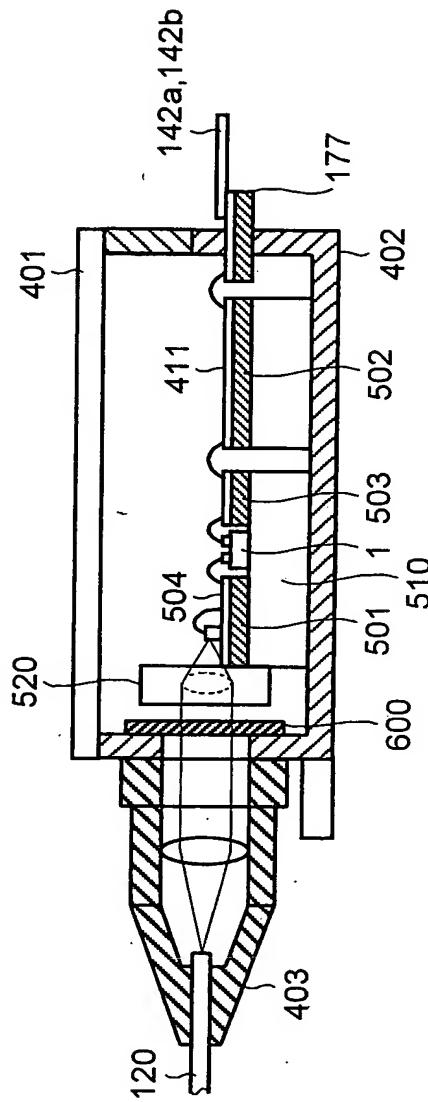


FIG. 18

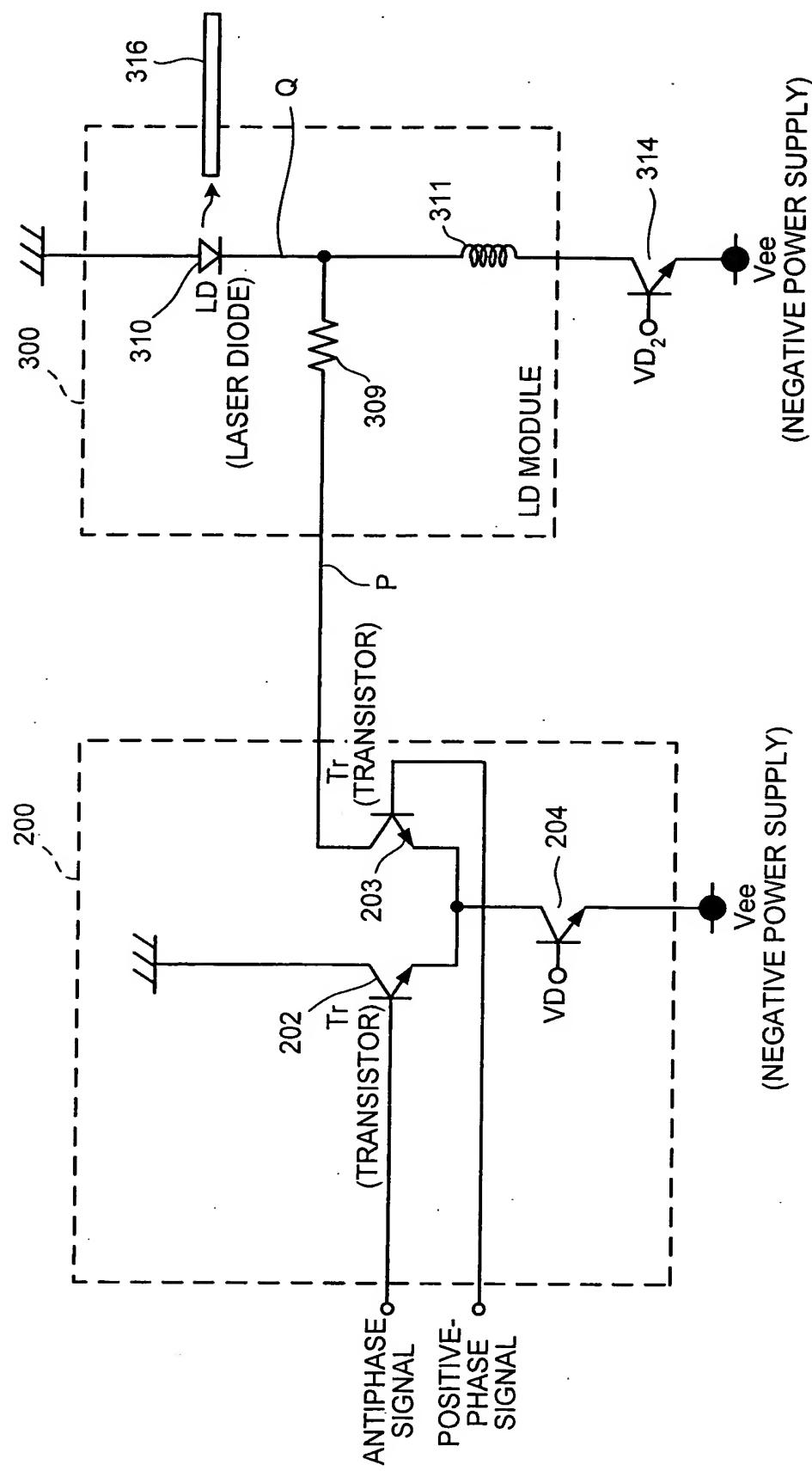


FIG. 19

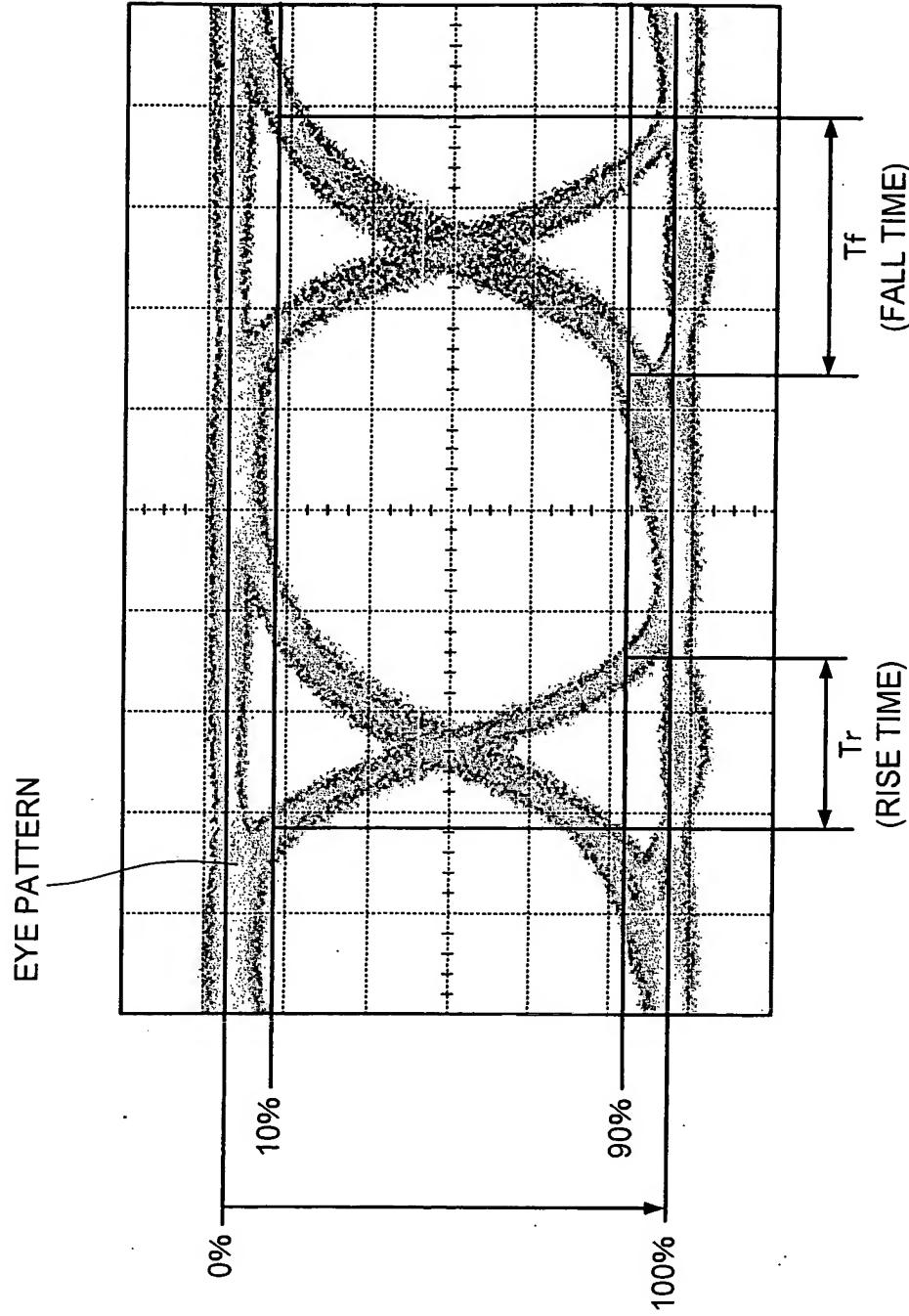
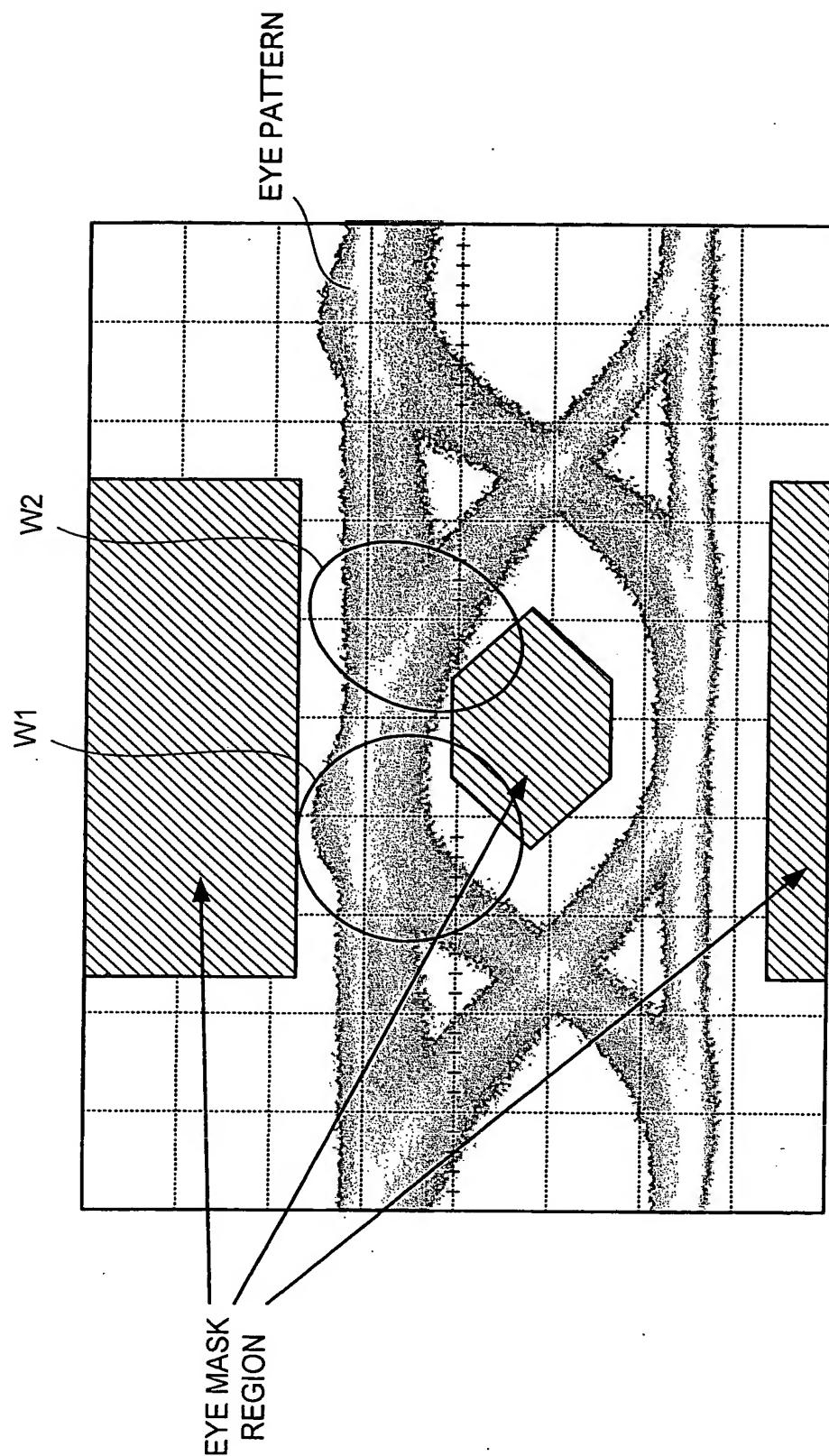


FIG.20



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FIG.21

